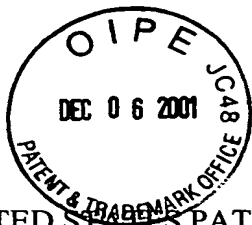


212740US-2SRD CONT



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Yoshiyuki MATSUNAGA, et al.

SERIAL NO: 09/927,632

FILED: August 13, 2001

FOR IMAGE SYSTEM, SOLID-STAGE :
IMAGING SEMICONDUCTOR...

:

: GROUP ART UNIT: 2612

: EXAMINER: Unassigned

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PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Prior to a first examination on the merits, please amend the above-identified
application as follows:

IN THE TITLE

--IMAGE SYSTEM, SOLID-STATE IMAGING SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE USED IN THE IMAGE SYSTEM, AND DIFFERENCE
OUTPUT METHOD USED FOR THE IMAGE SYSTEM--

IN THE SPECIFICATION

On page 1, before the first line, insert:

--This application is a continuation of Application Serial No. 09/022,339, filed

February 11, 1998, which has been abandoned, which is a continuation of PCT/JP96/02285,
filed August 12, 1996, the disclosures of which are herein incorporated by reference.--